Router TURRIS

CPLD registers

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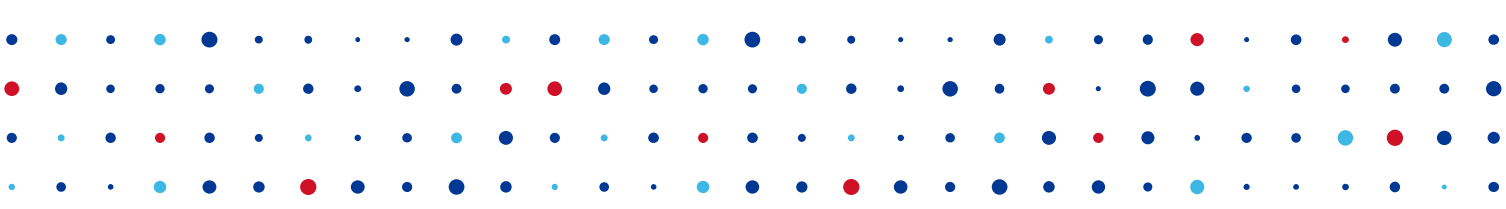


1. CPLD registers

CPLD VERSION

Register	cpld_ver	
Address	0x00	
Reset value	0x04	
bit7-bit4	0000	
bit3-bit0	CPLD version	R

PCB REVISION

Register	pcb_rev	
Address	0x01	
Reset value	0x03	
bit7-bit4	0000	
bit3-bit0	PCB revision	R

WATCHDOG CONFIGURATION

Register	wd_cfg	
Address	0x02	
Reset value	0x03	
bit7-bit3	00000	
bit2-bit0	wd_cfg	RW
0x03 (watchdog disable)		

SYSTEM RESET SWITCH, NOT USED!

Register	rst_bps_sw

Address 0x03
Reset value 0x00

bit7-bit1 0000000
bit0 rst_bps_sw R
0x00 (system reset is selected when manual reset is pushed down less than 3s)
bypass only on P1020UTM

SYSTEM RESET WATCHDOG, NOT USED!

Register rst_bps_wd
Address 0x05
Reset value 0x00

bit7-bit1 0000000
bit0 rst_bps_wd R
0x00 (system reset is selected when watchdog is over flow)
bypass only on P1020UTM

STATUS/WIFI LED

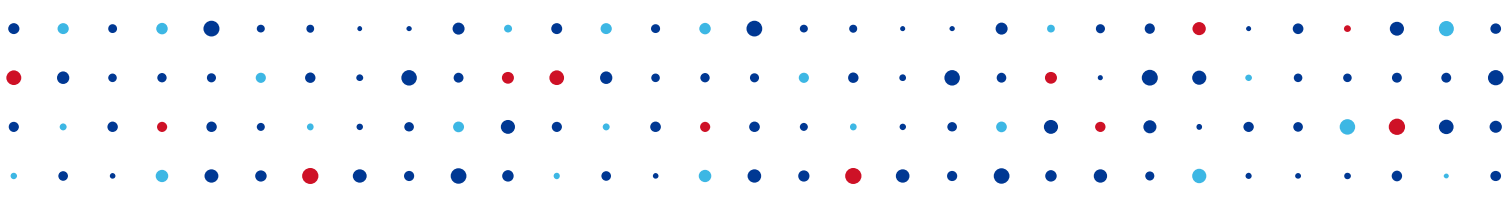
Register status/wifi LED
Address 0x08
Reset value 0x01

bit7-bit1 0000000
bit0 status/wifi LED RW
0x01 (status led is off)

FXO LED, NOT USED!

Register fxo LED
Address 0x09
Reset value 0x01

bit7-bit1 0000000
bit0 fxo LED



0x01 (one FXO LED off)

FXS LEDs, NOT USED!

Register	fxs LEDs
Address	0x0A
Reset value	0x0F

fxs LED[bit3:bit0]

bit7-bit4	0000
bit3	fxs LED3
bit2	fxs LED2
bit1	fxs LED1
bit0	fxs LED0

0x0F (four FXS LEDs off)

CFG_STROBE2 & CFG_STROBE2A, NOT USED!

Register	cfg_strobe2 & cfg_strobe2a
Address	0x0B
Reset value	0x00

cfg_strobe2 & cfg_strobe2a[bit1:bit0]

bit7-bit2	000000
bit1	cfg_strobe2
bit0	cfg_strobe2a

0x00 (multipled pin option output enabled)

SYSTEM RESET

Register	system_rst
Address	0x0D
Reset value	0x00

bit7-bit1	0000000	
bit0	system_rst	RW

0x00 (system is not reset)

CPLD SUB-VERSION

Register	cpld_ver_sub		
Address	0x12		
Reset value	0x01		
bit7-bit4	0000		
bit3-bit0	0001	cpld_ver_sub	R

WAN PORT RED LED INTENSITY

Register	light_pipe_wan_r	
Address	0x13	
Reset value	0xFF	
bit7-bit0	color intensity value	RW
0xFF	maximum color intensity	

WAN PORT GREEN LED INTENSITY

Register	light_pipe_wan_g	
Address	0x14	
Reset value	0xFF	
bit7-bit0	color intensity value	RW
0xFF	maximum color intensity	

WAN PORT BLUE LED INTENSITY

Register	light_pipe_wan_b	
Address	0x15	
Reset value	0xFF	
bit7-bit0	color intensity value	RW
0xFF	maximum color intensity	

LAN PORTS RED LED INTENSITY

Register	light_pipe_lan_r

Address	0x16
Reset value	0xFF

bit7-bit0	color intensity value	RW
0xFF	maximum color intensity	

LAN PORTS GREEN LED INTENSITY

Register	light_pipe_lan_g
Address	0x17
Reset value	0xFF

bit7-bit0	color intensity value	RW
0xFF	maximum color intensity	

LAN PORTS BLUE LED INTENSITY

Register	light_pipe_lan_b
Address	0x18
Reset value	0xFF

bit7-bit0	color intensity value	RW
0xFF	maximum color intensity	

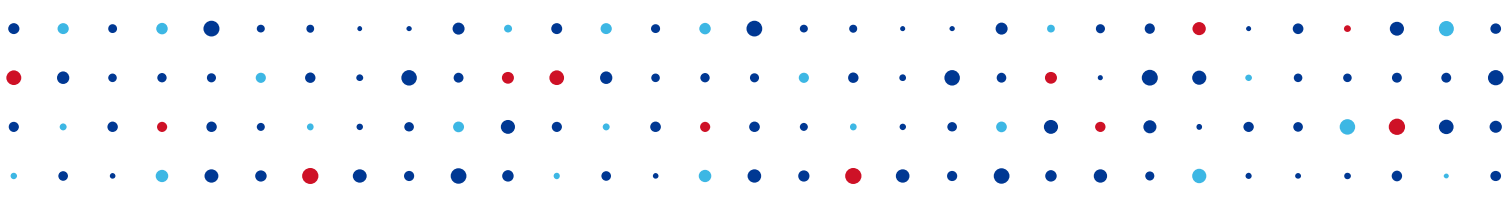
STATUS/WIFI RED LED INTENSITY

Register	light_pipe_s_r
Address	0x19
Reset value	0xFF

bit7-bit0	color intensity value	RW
0xFF	maximum color intensity	

STATUS/WIFI GREEN LED INTENSITY

Register	light_pipe_s_g
Address	0x1A
Reset value	0xFF



bit7-bit0	color intensity value	RW
0xFF	maximum color intensity	

STATUS/WIFI BLUE LED INTENSITY

Register	light_pipe_s_b
Address	0x1B
Reset value	0xFF

bit7-bit0	color intensity value	RW
0xFF	maximum color intensity	

POWER RED LED INTENSITY

Register	light_pipe_p_r
Address	0x1C
Reset value	0xFF

bit7-bit0	color intensity value	RW
0xFF	maximum color intensity	

POWER GREEN LED INTENSITY

Register	light_pipe_p_g
Address	0x1D
Reset value	0xFF

bit7-bit0	color intensity value	RW
0xFF	maximum color intensity	

POWER BLUE LED INTENSITY

Register	light_pipe_p_b
Address	0x1E
Reset value	0xFF

bit7-bit0	color intensity value	RW
0xFF	maximum color intensity	

RESET BUTTON PUSH DELAY

Register	reset_delay
Address	0x1F
Reset value	0xXX

bit7-bit0	reset delay intervals	R
bit7	0	R
bit6	reset_6s_on	R
bit5	reset_5s_on	R
bit4	reset_4s_on	R
bit3	reset_3s_on	R
bit2	reset_2s_on	R
bit1	reset_1s_on	R
bit0	reset_300ms_on	R

GLOBAL LED SW INTENSITY LEVEL

Register	led_intensity_level_sw
Address	0x20
Reset value	0x07

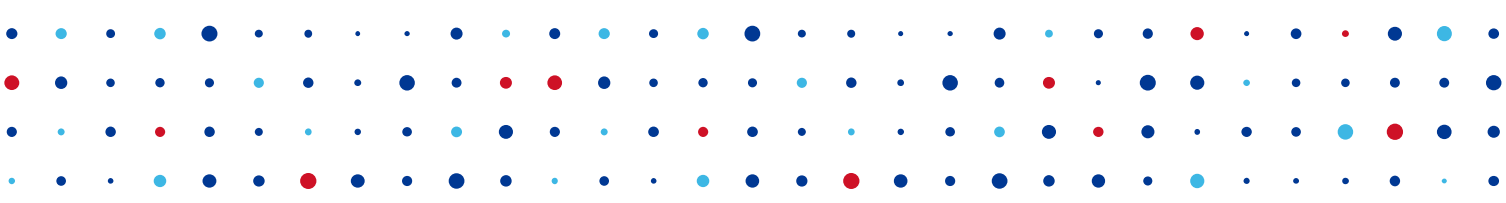
bit7-bit3	00000		
bit2-bit0	111	led_intensity_level_sw	RW

GLOBAL LED SW INTENSITY

Register	led_intensity_sw
Address	0x21
Reset value	0xFF

bit7-bit0	11111111	led_intensity_sw	R

LED SW OVERRIDE



Register	led_sw_override
Address	0x22
Reset value	0x00

bit7	enable sw control of POWER LED	RW
bit6	enable sw control of STATUS/WIFI LED	RW
bit5	enable sw control of LAN5 LED	RW
bit4	enable sw control of LAN4 LED	RW
bit3	enable sw control of LAN3 LED	RW
bit2	enable sw control of LAN2 LED	RW
bit1	enable sw control of LAN1 LED	RW
bit0	enable sw control of WAN LED	RW

LED SW ENABLE

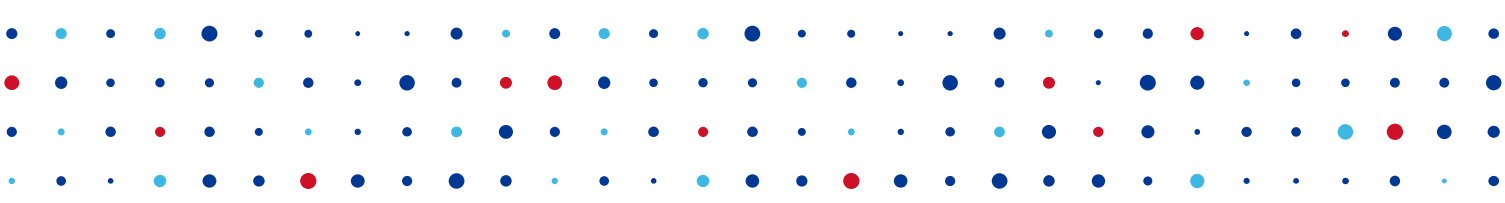
Register	led_sw_enable
Address	0x23
Reset value	0x00

bit7	enable POWER LED	RW
bit6	enable STATUS/WIFI LED	RW
bit5	enable LAN5 LED	RW
bit4	enable LAN4 LED	RW
bit3	enable LAN3 LED	RW
bit2	enable LAN2 LED	RW
bit1	enable LAN1 LED	RW
bit0	enable WAN LED	RW

INTENSITY LEVEL 7 VALUE

Register	mixed_rgb_intensity_level7_sw
Address	0x28
Reset value	0xFF

bit7-bit0	mixed_rgb_intensity_level7_sw	RW
0xFF	global intensity level 7 value	



INTENSITY LEVEL 6 VALUE

Register	mixed_rgb_intensity_level6_sw
Address	0x29
Reset value	0x40

bit7-bit0	mixed_rgb_intensity_level6_sw	RW
0x40	global intensity level 6 value	

INTENSITY LEVEL 5 VALUE

Register	mixed_rgb_intensity_level5_sw
Address	0x2A
Reset value	0x20

bit7-bit0	mixed_rgb_intensity_level5_sw	RW
0x20	global intensity level 5 value	

INTENSITY LEVEL 4 VALUE

Register	mixed_rgb_intensity_level4_sw
Address	0x2B
Reset value	0x10

bit7-bit0	mixed_rgb_intensity_level4_sw	RW
0x10	global intensity level 4 value	

INTENSITY LEVEL 3 VALUE

Register	mixed_rgb_intensity_level3_sw
Address	0x2C
Reset value	0x08

bit7-bit0	mixed_rgb_intensity_level3_sw	RW
0x08	global intensity level 3 value	

INTENSITY LEVEL 2 VALUE

Register	mixed_rgb_intensity_level2_sw	
Address	0x2D	
Reset value	0x04	
bit7-bit0	mixed_rgb_intensity_level2_sw	RW
0x04	global intensity level 2 value	

INTENSITY LEVEL 1 VALUE

Register	mixed_rgb_intensity_level1_sw	
Address	0x2E	
Reset value	0x02	
bit7-bit0	mixed_rgb_intensity_level1_sw	RW
0x02	global intensity level 1 value	

INTENSITY LEVEL 0 VALUE

Register	mixed_rgb_intensity_level0_sw	
Address	0x2F	
Reset value	0x00	
bit7-bit0	mixed_rgb_intensity_level0_sw	RW
0x00	global intensity level 0 value	